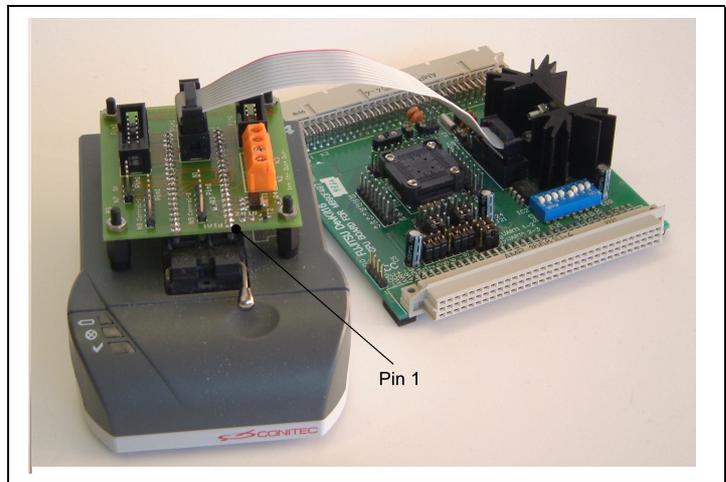
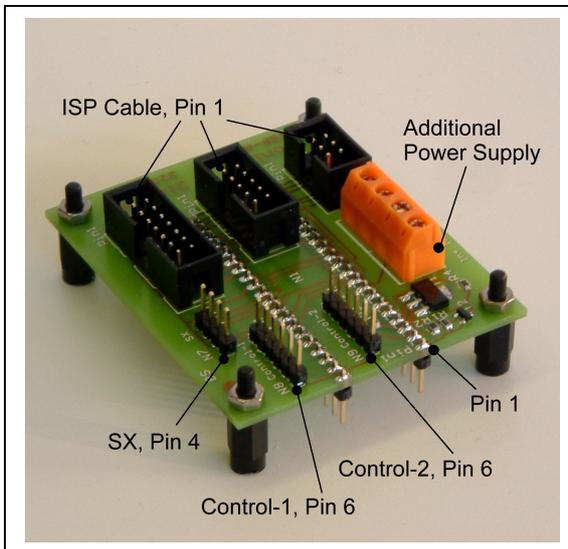


-- English --

Adapter for MCU's, which are in system programmable.

-- Deutsch --

Adapter für MCU's, die im System programmiert werden können.



## ISP Adapter

Conitec Part No. 210865

(Valid for SW Release version > 1.16.11)

### Common ISP adapter Functions for ISP Devices

Galep4 Pin No.	ISP Connector function	Description
40	WDT	Watch-Dog Trigger output. Activated in "ISP Options"
2	POWER_ON	Switch for external Power supply – controlled by Galep SW Active with Logic HIGH Level (3,3V or 5V) Activated in "ISP Options"
1	GND	Common GND for POWER_ON, WDT, OUT- and IN- signals.
	IN+ IN-	External VCC input, IN- is connected to GND
	OUT+ OUT-	External VCC output. This Vcc is switched by the POWER_ON signal. OUT- is connected to GND

Some of the Galep output pins are connected parallel by the ISP-adapter. This lowers the Vcc Drop-Down effect caused by Pin-Driver resistors and enables the Galep to support up to 200mA.

In case of these limited Galep4 power supply capabilities, the ISP-Adapter contains an additional power switch control (POWER\_ON pin) and power transistors.

The Galep SW is then able to control a target-Systems which require more than 200mA (Enabled by ISP-Options: „Adapter Ext. Power ON“).

If an external power supply switch is used, the user have to support the IN-Pins with the required Voltage (max. 2A/20V).

If the Target-System requires an other voltage than the Galep does provide (s. ISP- Options Menu: „Vcc target Supply“), the external power supply feature have to be used too.

An ISP-adapter isn't mandatory if a power supply below 50mA is required. An 1:1 connection (s. Tables below) can be used in this case.

## ATMEL Devices

Conitec Part No. 210865, Connector position N4.

The pin connections for ISP mode:

Galep3 Pin No.	Galep4 Pin No.	ISP Connector definition by ATMEL	ISP Connector function
21	21	1	MISO
20	20	2	Vcc *
22	22	3	SCK
19	19	4	MOSI
23	23	5	/RESET
30	18	6	GND

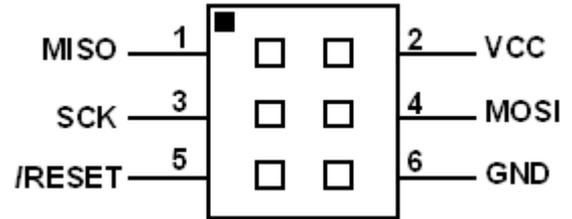


Fig. 1: ISP connection, connector N4

\* - Vcc from GALEP to target. Can be connected/disconnected via the Options Dialog.

Conitec Part No. 210865, Connector position N5.

The pin connections for ISP mode:

Galep3 Pin No.	Galep4 Pin No.	ISP Connector definition by ATMEL	ISP Connector function
21	21	1	SCK
20	20	2	GND
22	22	3	MISO
29	19	4	Vcc *
23	23	5	/RESET
		6	NC
		7	NC
		8	NC
25	25	9	MOSI
16	16	10	GND

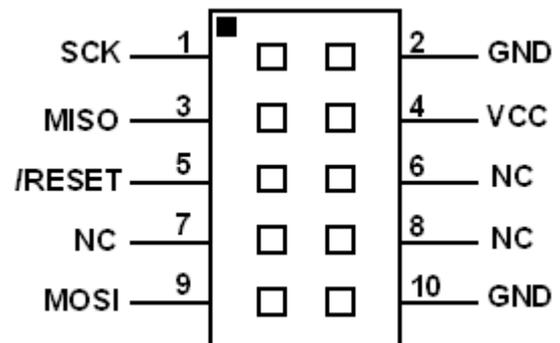


Fig. 2: ISP connection, connector N5

\* - Vcc from GALEP to target. Can be connected/disconnected via the Options Dialog.

**JTAG Interface selection in device menu**

The pin connections for JTAG mode („JTAG“ selection in Galep4 device list):

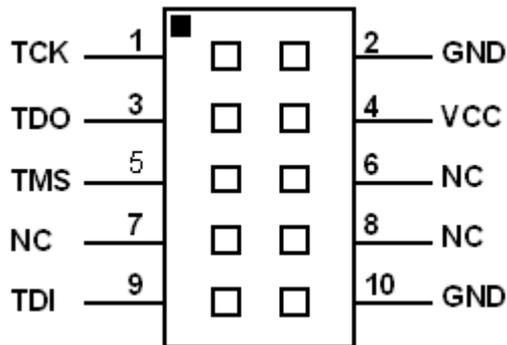


Fig. 3: JTAG connection, connector N5

Galep4 Pin No.	ISP Connector function	ATMEL ISP Connector definition  or Galep ISP adapter Part No. 210865 Connector N5(10pol.)  Pin No.:	Note
16	GND	10	
17	NC	8	
18	NC	6	
19	NC (VCC)	4	Some programming interfaces require a power supply by the target. This pin is not connected to Galep power supply and not supported by Galep.
20	GND	2	
21	TCK	1	
22	TDO	3	
23	TMS	5	
24	NC	7	
25	TDI	9	

## FUJITSU Devices

Conitec Part No. 210865, Connector position N5.

See "ISP Options" for selecting the ISP functions.

Activate „Vcc by Galep“ in "ISP Options" menu if no other power source is available.

**Caution: Deactivate „VCC by Galep“ in "ISP Options" menu when Target don't use the Power supply by Galep4– Target or Galep device damage is possible.**

### „SYNC“ adapter selected in device menu

ISP cable length max. 20cm. A cable length of up to 60cm can be used if 220Ohm resistors are added in serial of P01, P00, SCK, SIN, SOT, MD0 and MD2 wire.

Selection of cable length >20cm and if 220Ohm have to be used, depends on target layout and have to be selected by the user.

Check FUJITSU application notes AN-000004-11 for more informations.

The pin connections for synchronous mode („SYNC“ selection in Galep4 device list):

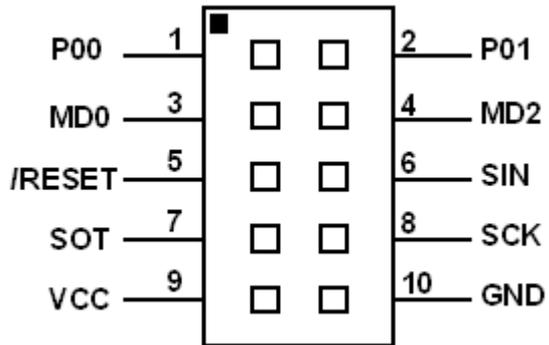


Fig. 4: Fujitsu SYNC connection, connector

Galep4 Pin No.	ISP Connector function	ISP Connector definition by Fujitsu  or Galep ISP adapter Part No. 210865 Connector N5(10pol.) or N6(14pol.)  Pin No.:	Note
16	GND	10	
17	SCK	8	
18	SIN	6	
19	MD2	4	
20	P01 (P31, P81)	2	This pin is used as a BUSY signal for Galep SW when MCU kernel is running. It Should be connected to Galep for best performance. Otherwise fixed delay times are assumed by Galep SW. Don't connect this pin without pull-up or pull-down to VCC or GND. This could damage the device or programmer during SYNC mode.
21	P00 (P30, P80)	1	
22	MD0	3	
23	/RESET	5	
24	SOT	7	
25	VCC	9	

See Fujitsu Application Notes shows how to connect the 10 pins to your target system.

**„ASYNC“ adapter selected in device menu**

In asynchronous mode you need a PC RS232 Port only (signals RxD and TxD at least). A Galep4 is mandatory to be connected as a dongle.

The pin connections for asynchronous mode („ASYNC“ selection in Galep4 device list):

PC RS232, DB-9 PIN No.:	Connector function	Note
1	NC	
2	RxD	
3	TxD	
4	DTR	Used for Reset signal or Mode Control s. „RTS reset, DTR mode selection“
5	GND	
6	DTR	Connected to DTR
7	RTS	Used for Reset signal or Mode Control s. „RTS reset, DTR mode selection“
8	CTS	Connected to RTS
9	NC	

If „ASYNC“ is being selected, the ISP adapter (Part No. 210865) is mandatory only, when GALEP have to provide the target power supply (200mA max.) or the Galep have to generate a Watch-Dog trigger signal.

If a target clock sources of 5MHz, 10MHz or 20MHz is used, some targets require to set P00 or P30 or P80 to logic HIGH.

For more details check Fujitsu Application Notes how to connect a RS232 to your target system.

## Motorola Devices

Conitec Part No. 210865, Connector position N4.

### BDM Interface selection in device menu

The pin connections for BDM mode („BDM“ selection in Galep4 device list):

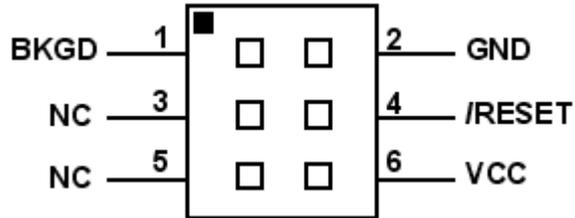


Fig. 5: Motorola BDM connection, N4

Galep4 Pin No.	ISP Connector function	ISP Connector definition by Motorola or Galep ISP adapter Part No. 210865 Connector N4(6pol.) Pin No.:
18	VCC	6
19	/RESET	4
20	GND	2
21	BKGD	1
22	NC	3
23	NC	5

## MICROCHIP Devices

(TBD)

### ISP Options

Common options for all devices using the ISP-Adapter.

#### **Vcc by Galep**

This switch enables the power supply by Galep for a target system. The voltage depends on the selected device and is visible below.

This switch can also be activated when a COM port is used for programming. This switch is disabled if „Adapter Ext. Power ON“ is selected.

#### **Adapter Ext. Power ON**

This switch enables a control pin for external power supply switching.

If the signal is raised to HIGH level during start of device initialization, a 100ms delay is added to respect external Vcc raise time.

Activating this switch disables the „Vcc by Galep“ switch.

If not enabled the output will be HIGH-Z.

#### **Reset by Galep**

This switch enables the reset signal for the target system.

The duration of active phase and active polarity can be selected.

Depending on the ISP-SW algorithm, a reset will be activated by a SW-command too (transmitted via the communication lines).

If mandatory by the device, this signal is fixed to active.

Depending of the selected connection, Reset is active only at ISP adapter or at RS232 connector lines.

#### **Reset enable/Pulse time**

Define the Reset period length.

#### **Reset enable/Polarity**

This button is used to respect signal inverter on the target system.

#### **WDT Trigger by Galep**

Activates a Watch-Dog trigger signal during an action is running.

If enabled, the duration of the trigger periods can be selected.

This signal is output by the ISP adapter.

If not enabled the output will be HIGH-Z.

This switch is also active when a COM port is used for programming.

#### **Default Values**

Reset all values.

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## **FUJITSU (ASYNCR/SYNCR)**

If a device is being selected with „SYNCR“ or „ASYNCR“, this options menu is visible.

„SYNCR“ in device list means a synchronous connection (fast connection with SDIN, SDOUT and SCLK signals). The ISP adapter is mandatory in this case.

„ASYNCR“ in device list means an asynchronous connection.  
A PC COM Port have to be used.

The target system have to fulfil the requirements for synchron or asynchron programming (see manufacturers design notes).

If „ASYNCR“ is being selected, an ISP adapter is only mandatory when GALEP have to provide the target power supply or have to generate a Watch-Dog trigger signal.

### **ASYNCR Options**

If a device with „ASYNCR“ adapter is selected the following options are visible too:

#### **COMPort**

One of PC COM ports COM1 ..COM3 can be selected for connection.

#### **Baud Rate**

Active when RS232 communication speed of the Target system can be selected.

The signals RTS and DTR are used for Reset and Control functions.

#### **RTS reset, DTR mode selection**

RTS signal on selected COM port is toggled at reset.

DTR signal is used for mode selection.

#### **DTR reset, RTS mode selection**

DTR signal on selected COM port is toggled at reset.

RTS signal is used for mode selection.

#### **CPU mode Polarity**

Select polarity of CPU mode when active.

This button is used to respect signal inverter on the target system.

#### **MCU clock**

Select target MCU Crystal frequency to compute the correct Baudrate.

#### **Default Values**

Reset all values.

### **SYNCR Options**

If a device with „SYNCR“ adapter is selected the following option is visible too:

#### **Sync Speed**

Selects Bit I/O clock from 125kHz up to 1MHz. Respect Sync clock max. 1/8 of crystal frequency.

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## **MOTOROLA (BDM)**

### **BDM COM Options**

**Don't enable the „Vcc by Galep“ switch. VCC in BDM interface is defined to support the Programming interface and not to be supported by the interface.**

**In case of limited Galep FPGA clock source generation, the devices are initialised in active background mode during Reset.**

Check the Device documentation if any of the values below are changed.

#### **MCU Bus Clock**

Select MCU bus Clock ( $\frac{1}{2}$  of Crystal frequency) to process a new FCDIV value for the internal MCU Programming clock (150kHz -200kHz required).

#### **FCDIV\_Register**

Hex Value for FCDIV register changed if MCU Bus clock is changed or by user edit.

#### **Alternate Clock**

Select value of communication alternate clock of BDM interface

#### **Enable ACK Signal**

Enables Acknowledge signal for communication.

#### **Select alternate Frequency**

The value selected in „**Alternate Clock**“ is used as a base frequency for communication.