AVR® JTAG ICE

ON-CHIP DEBUG SYSTEM



The AVR JTAG ICE from Atmel is a powerful development tool for On-chip Debugging of all AVR® 8-bit RISC microcontrollers with IEEE 1149.1 compliant JTAG interface. The JTAG ICE and the AVR Studio® user interface give the user complete control of the internal resources of the microcontroller, helping to reduce development time by making debugging easier. The JTAG ICE performs real-time emulation of the microcontroller while it is running in a target system. The JTAG ICE provides emulation capability at a fraction of the cost of traditional emulators. The JTAG ICE include the following features:

- Interfaces with AVR Studio version 3.52 or newer
- Emulates all On-chip Functions, both Digital and Analog
- Supports Break on Change of Program Memory Flow
- RS-232 Interface to PC

- Supports Data and Program Memory Breakpoints
- Supports C and Assembler Source Level Debugging
- Supports In-System Programming via the JTAG Interface
- Directly Powered from Target Board or External Power Supply for 9 - 15V DC Power





Corporate Headquarters

2325 Orchard Parkway San Jose, CA 95131 Tel: (408) 441-0311 Fax: (408) 487-2600

Europe

Atmel SarL Route des Arsenaux 41 Casa Postale 80 CH-1705 Fribourg Switzerland

Tel: (41) 26-426-5555 Fax: (41) 26-426-5500

Asia

Atmel Asia, Ltd Room 1219 Chinachem Golden Plaza 77 Mody Road Tsimshatsui East, Kowloon Hong Kong Tel: (852) 2721-9778 Fax: (852) 2722-1369

Japan

Atmel Japan K.K. 9F, Tonetsu Shinkawa Bldg. 1-24-8 Shinkawa Chuo-ku, Tokyo 104-0033 Japan Tel: (81) 3-3523-3551

Tel: (81) 3-3523-3551 Fax: (81) 3-3523-7581

e-mail

literature@atmel.com

Web Site

http://www.atmel.com

©Atmel Corporation 2001

Atmel Corporation makes no warranty for the use of its products, other than those expressly contained in the Company's standard warranty which is detailed in Atmel's Terms and Conditions located on the Company's web site. The Company assumes no responsibility for any errors which may appear in this document, reserves the right to change devices or specifications detailed herein at any time without notice, and does not make any commitment to update the information contained herein. No licenses to patents or other intellectual property of Atmel are granted by the Company in connection with the sale of Atmel products, expressly or by implication. Atmel's products are not authorized for use as critical components in life support devices or systems.

Atmel®, AVR®, and AVR Studio® are registered trademarks of Atmel Corporation.

Other terms and product names in this document may be trademarks of others.

2489A-09/01/15M

The JTAG ICE allows access to all the powerful features of the AVR microcontroller. All AVR resources can be monitored: Flash memory, EEPROM memory, SRAM memory, register file, program counter, fuse and lock bits, and all I/O modules. The JTAG ICE also offers extensive On-chip Debug support for break conditions, including break on change of program memory flow, program memory breakpoints on single address or address range, and data memory breakpoints on single address or adress range.

The JTAG ICE supports the following Atmel AVR devices:

- ATmega323
- ATmega16
- ATmega128
- ATmega32 (2Q2002)
- ATmega162 (1Q2002)

The JTAG ICE will automatically be upgraded by future AVR Studio releases to support future devices with JTAG support as they are released.



The JTAG ICE interface is integrated in AVR Studio, Atmel's front-end tool for development on the AVR architecture. All phases of the AVR development can be done in this Integrated Developing Environment.

Ordering Information for the AVR JTAG ICE

The JTAG ICE is available from all of Atmel's franchised distributors. The ordering code is ATJTAGICE. The latest version of AVR Studio is available from the Atmel web site, www.atmel.com.